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333312 (33)

BE (3rd Semester)

Examination, April-May 2021 Branch : CSE, IT, Mechatronics

DIGITAL ELECTRONICS & LOGIC DESIGN

Time Allowed : Three Hours

Maximum Marks : 80

Minimum Pass Marks : 28

Note : Part (a) is compulsory. Attempt any two parts

from (b), (c) and (d).

Q. 1. (a) (i) Convert a gray code 1001 1011 into

binary.

1

1

(ii) Convert (3C9A)₁₆ into decimal.

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P.T.O.

(b) Simplify the following Boolean function by using Quine McCluskey method.
7

 $f(A, B, C, D) = \sum m(0, 1, 2, 8, 10, 11, 14, 15)$

(c) Simplify following Bollean function using K-map also draw the logic gate circuit. **7** $f(A, B, C, D) = \sum m(0, 2, 3, 5, 6, 7, 8, 9)$

+ d(10, 11, 12, 13, 14, 15)

(d) Simplify Boolean function in sum of product.

(i) $(X+Z) (Z\overline{Y}+XZ) (\overline{X}Z+\overline{Y})$ 4

(ii) $[A\overline{B}(C+BD)+\overline{A}\overline{B}]C$ 3

Q. 2. (a) Explain Demux. 2

(b) Design a BCD to Excess 3 code converter using AND, OR, EXOR.

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	(c)	Implement the following Boolean function	tion
	1.315	using 4 : 1 MUX.	7
¢.		$f(A, B, C, D) = \sum m (0,1, 2, 4, 6, 9, 12, 12)$	14)
n Alina	(d)	Explain BCD adder with diagram.	7
Q. 3.	(a)	What do you mean by flip flop?	2
	(b)	Convert SR FF into JK. Also draw	logic
	81121-24	diagram.	7
	(c)	Draw the circuit of RTL using NOR ga	ate &
		explain it.	7
	(d)	Explain race around condition in JK	F/F.
	s.n. s	Suggest a suitable modification in JK	F/F to
		overcome this problem.	7
Q. 4	. (a) Write two applications of shift register	er. 2
	(b	b) Explain shift register with its classificat	ions. 7
	(0	b) Design divide by 6 counter using	T F/F.
		Write state table & reduce the expl	ression
		using K Map. Also draw logic diagra	ım. 7

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(22) P.T.O.

1. 	d) Explain ripple counter (2 bit) with logic
	diagram & timing diagram using JK FF. 7
Q. 5. () Difference between RAM & ROM. 2
() Discuss the successive approximation
	method for A/D conversion. 7
(0	Implement the following Boolean function
	using PLA. 7
	$f_1(A, B, C) = \sum (0, 1, 3, 4)$
	f_2 (A, B, C) = Σ (1, 2, 3, 4, 5)
(d	Explain ROM & draw 8×4 diode matrix

ROM with the help of decoder (3×8). 7